

NSS30071MR6T1G

30 V, 0.7 A, Low $V_{CE(sat)}$ NPN Transistor

ON Semiconductor's e²PowerEdge family of low $V_{CE(sat)}$ transistors are miniature surface mount devices featuring ultra low saturation voltage ($V_{CE(sat)}$) and high current gain capability. These are designed for use in low voltage, high speed switching applications where affordable efficient energy control is important.

Typical application are DC-DC converters and power management in portable and battery powered products such as cellular and cordless phones, PDAs, computers, printers, digital cameras and MP3 players. Other applications are low voltage motor controls in mass storage products such as disc drives and tape drives. In the automotive industry they can be used in air bag deployment and in the instrument cluster. The high current gain allows e²PowerEdge devices to be driven directly from PMU's control outputs, and the Linear Gain (Beta) makes them ideal components in analog amplifiers.

- These Devices are Pb-Free and are RoHS Compliant

MAXIMUM RATINGS ($T_C = 25^\circ\text{C}$ unless otherwise noted)

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	V_{CEO}	30	V
Collector-Base Voltage	V_{CBO}	40	V
Emitter-Base Voltage	V_{EBO}	5.0	V
Collector Current	I_C	700	mA
Base Current	I_B	350	mA
Total Power Dissipation @ $T_C = 25^\circ\text{C}$	P_D	342	mW
Total Power Dissipation @ $T_C = 85^\circ\text{C}$	P_D	178	mW
Thermal Resistance - Junction-to-Ambient (Note 1)	$R_{\theta JA}$	366	$^\circ\text{C}/\text{W}$
Total Power Dissipation @ $T_C = 25^\circ\text{C}$	P_D	665	mW
Total Power Dissipation @ $T_C = 85^\circ\text{C}$	P_D	346	mW
Thermal Resistance - Junction-to-Ambient (Note 2)	$R_{\theta JA}$	188	$^\circ\text{C}/\text{W}$
Operating and Storage Temperature Range	T_J, T_{stg}	-55 to +150	$^\circ\text{C}$

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

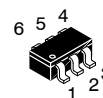
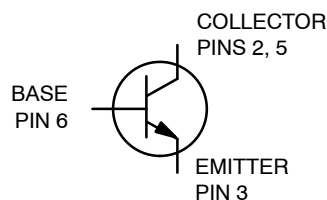
1. Minimum FR-4 or G-10 PCB, Operating to Steady State.
2. Mounted onto a 2" square FR-4 Board (1" sq 2 oz Cu 0.06" thick single sided), Operating to Steady State.



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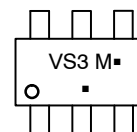
<http://onsemi.com>

30 VOLTS
0.7 AMPS
NPN LOW $V_{CE(sat)}$ TRANSISTOR
EQUIVALENT $R_{DS(on)}$ 200 m Ω



SC-74
CASE 318F
STYLE 2

MARKING DIAGRAM



VS3 = Specific Device Code
M = Date Code
▪ = Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

Device	Package	Shipping†
NSS30071MR6T1G	SC-74 (Pb-Free)	10000/Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure. BRD8011/D.

NSS30071MR6T1G

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Symbol	Characteristic	Min	Typ	Max	Unit
OFF CHARACTERISTICS					
V _{(BR)CBO}	Collector–Base Breakdown Voltage (I _C = 100 μAdc)	40	–	–	Vdc
V _{(BR)CEO}	Collector–Emitter Breakdown Voltage (I _C = 10 mAdc)	30	–	–	Vdc
V _{(BR)EBO}	Emitter–Base Breakdown Voltage (I _E = 100 μAdc)	5.0	–	–	Vdc
I _{CBO}	Collector Cutoff Current (V _{CB} = 25 Vdc, I _E = 0 Adc) (V _{CB} = 25 Vdc, I _E = 0 Adc, T _A = 125°C)	–	–	1.0 10	μAdc
I _{EBO}	Emitter Cutoff Current (V _{EB} = 5.0 Vdc, I _C = 0 Adc)	–	–	10	μAdc
ON CHARACTERISTICS					
h _{FE}	DC Current Gain (V _{CE} = 3.0 Vdc, I _C = 100 mAdc)	150	–	–	Vdc
V _{CE(sat)}	Collector–Emitter Saturation Voltage (I _C = 500 mAdc, I _B = 50 mAdc)	–	–	0.25	Vdc
V _{CE(sat)}	Collector–Emitter Saturation Voltage (I _C = 700 mAdc, I _B = 70 mAdc)	–	–	0.4	Vdc
V _{BE(sat)}	Base–Emitter Saturation Voltage (I _C = 700 mAdc, I _B = 70 mAdc)	–	–	1.1	Vdc
V _{BE(on)}	Base–Emitter Turn–On Voltage (I _C = 700 mAdc, V _{CE} = 1.0 Vdc)	–	–	1.0	Vdc

NSS30071MR6T1G

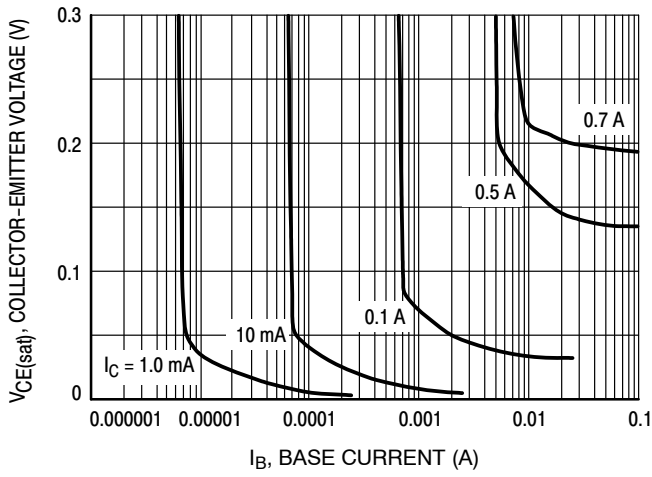


Figure 1. Collector Saturation Region

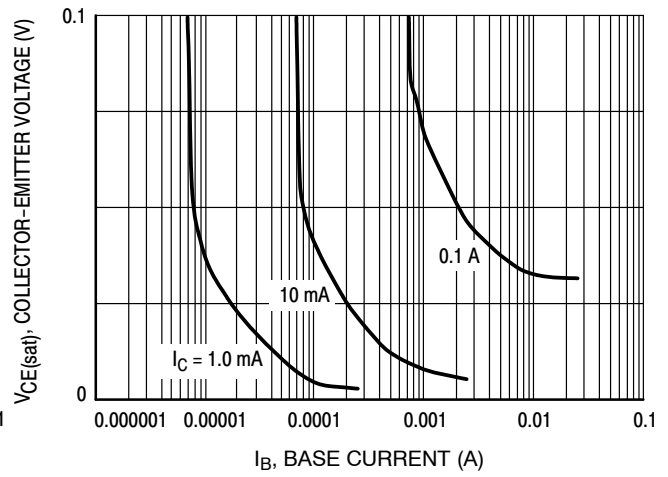


Figure 2. Collector Saturation Region

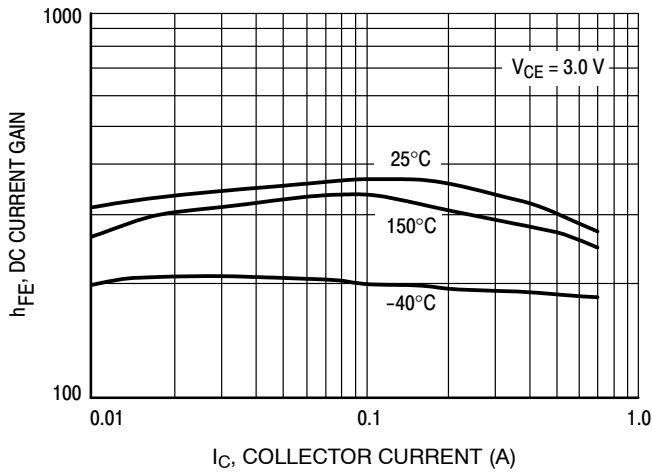


Figure 3. DC Current Gain

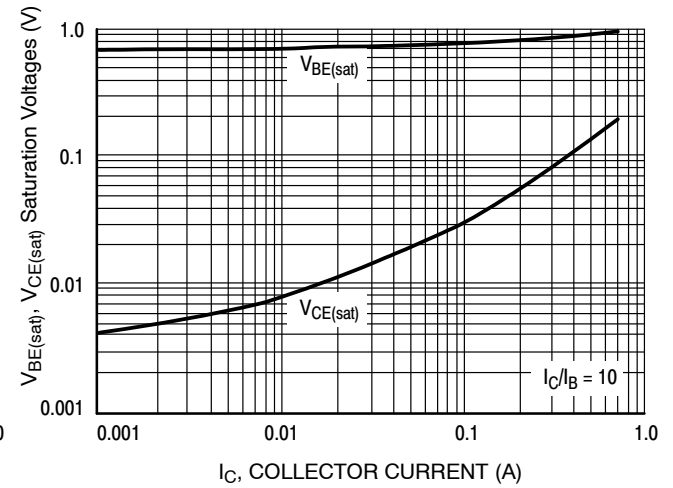


Figure 4. "SAT" Voltages

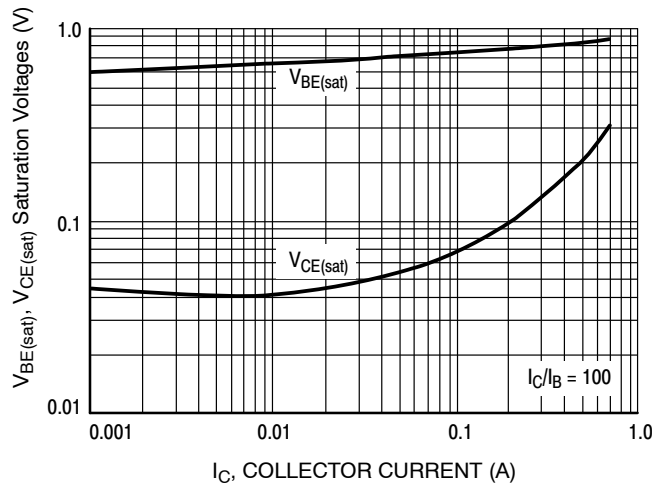


Figure 5. "SAT" Voltages

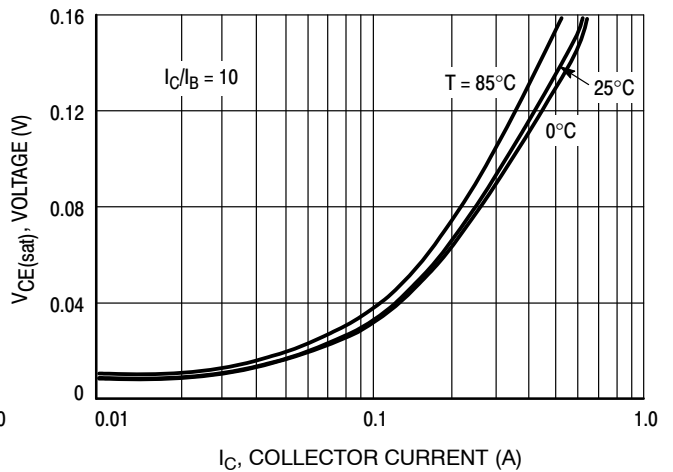


Figure 6. Collector-Emitter Saturation Voltage

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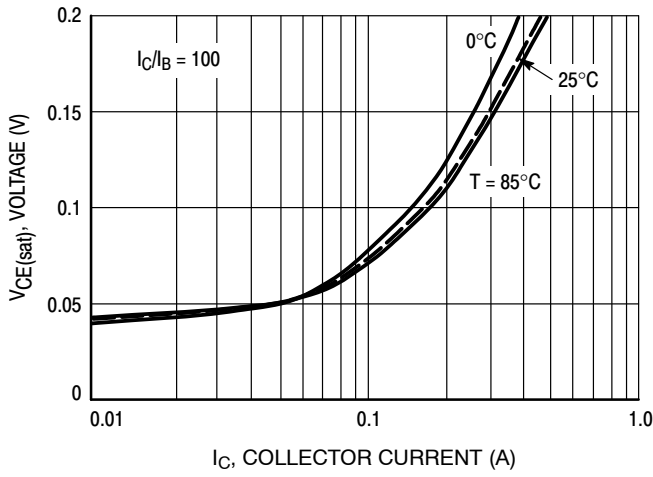


Figure 7. Collector-Emitter Saturation Voltage

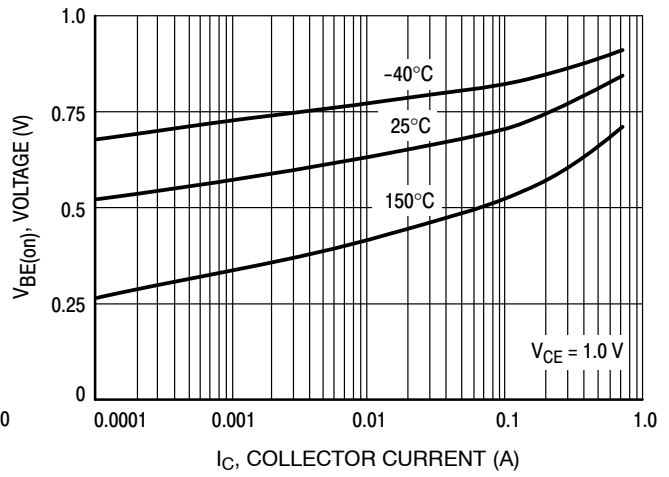


Figure 8. $V_{BE(on)}$ Voltage

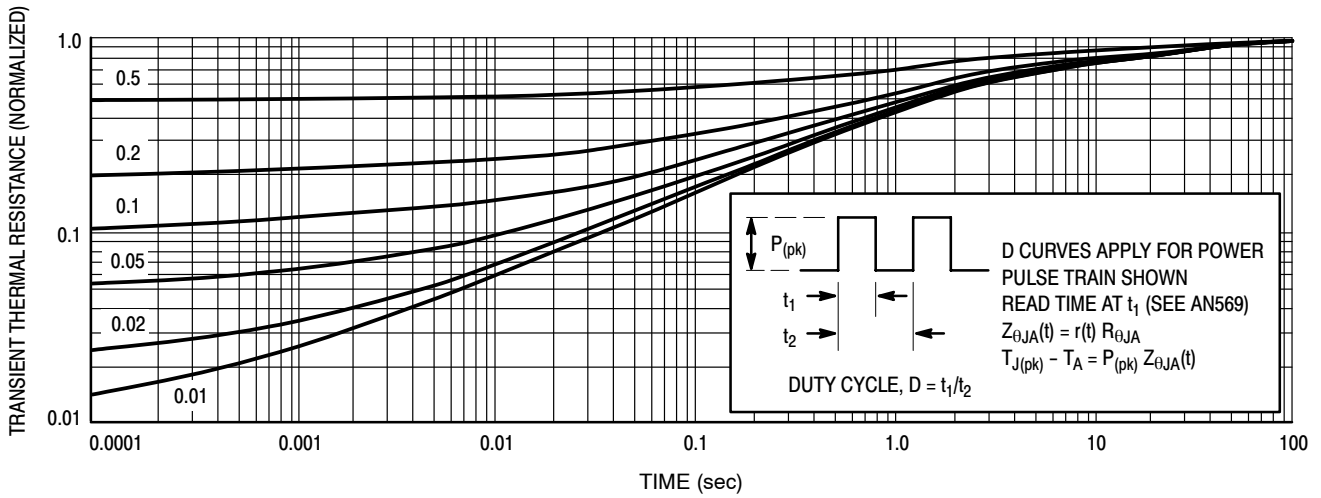
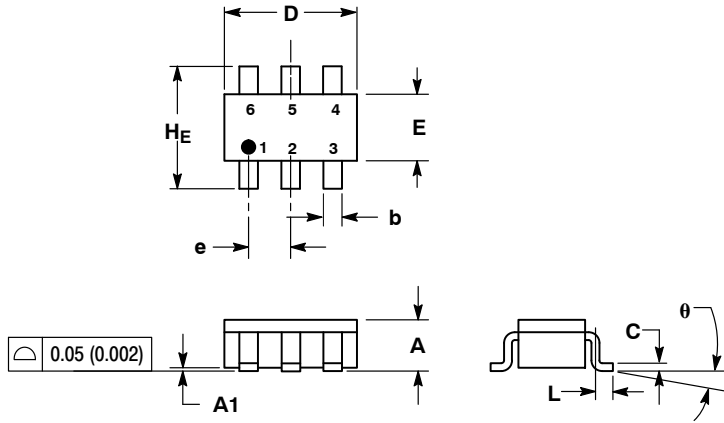


Figure 9. Thermal Response Curve

NSS30071MR6T1G

PACKAGE DIMENSIONS

SC-74
CASE 318F-05
ISSUE N



NOTES:

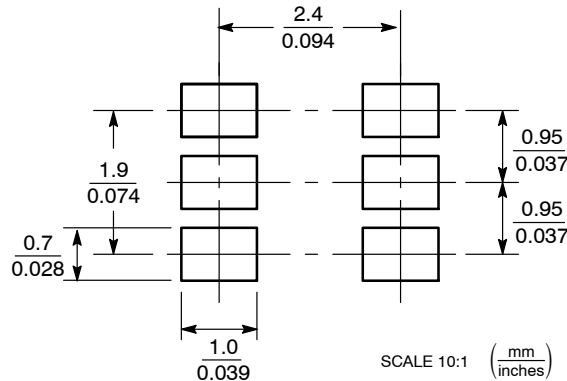
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.
3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.
4. 318F-01, -02, -03, -04 OBSOLETE. NEW STANDARD 318F-05.

DIM	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.90	1.00	1.10	0.035	0.039	0.043
A1	0.01	0.06	0.10	0.001	0.002	0.004
b	0.25	0.37	0.50	0.010	0.015	0.020
c	0.10	0.18	0.26	0.004	0.007	0.010
D	2.90	3.00	3.10	0.114	0.118	0.122
E	1.30	1.50	1.70	0.051	0.059	0.067
e	0.85	0.95	1.05	0.034	0.037	0.041
L	0.20	0.40	0.60	0.008	0.016	0.024
HE	2.50	2.75	3.00	0.099	0.108	0.118
θ	0°	-	10°	0°	-	10°

STYLE 2:

- PIN 1. NO CONNECTION
- COLLECTOR
- EMITTER
- NO CONNECTION
- COLLECTOR
- BASE

SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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